

Preliminary Datasheet

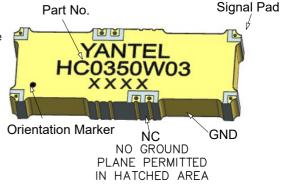
Hybrid Coupler 3 dB, 90°

Rev A0

Description

The HC0350W03 is a low profile, high performance 3dB hybrid coupler in a new easy to use, manufacturing friendly surface mount package. The HC0350W03 is particularly for balanced power and low noise amplifiers, plus signal designed distribution and other applications where low insertion loss and tight amplitude and phase balance is required. It can be used in high power applications up to 50 Watts.

Parts have been subjected to rigorous qualification testing and they are manufactured using materials with coefficients of thermal expansion (CTE) compatible with common substrates such as FR4, G-10, RF-35, RO4350 and polyimide.



Features:

- 100-605 MHz
- High Power
- Very Low Loss
- Tight Amplitude Balance
- High Isolation
- Low VSWR
- Good Repeatability
- CTE compatible with FR4, G-10, RF-35, RO4350B and polyimide
- Immersion gold, prevent surface oxidation & scratch
- RoHS Compliant

Electrical Specifications

Frequency	Isolation	Insertion Loss	Return Loss
MHz	dB Min	dB Max	dB
100 - 605	15	0.7	16.5
Amplitude Balance	Phase Balance	Power	Operating Temp.
-		Power Avg. CW Watts	

Notes:

1. All the above data are based on specified demo board.

2. Insertion loss:Thru board loss has been removed.

TOP VIEW SIDE VIEW **BOTTOM VIEW** 50.80 [2.000] -37 21.54 [0.848] NC 46.70 [1.839] Pin₃ Pin 18.62 [0.733] ₽in3 .02 [0.040] ыğ TXITXI YANTEL 19.04 [0.750] 15.04 [0.592] HC0350W03 6.86 [0.270] 2.05 [0.081] XXXX 1.89 [0.074] । হা হা 3.04 [0.120] NC Pin4 Pin¹ Pin1 Pin4 1.02 [0.040] 1.90 [0.075 Note: 1. NC represents no ground plane permitted in hatched area 2. All Dimensions show in mm[inch] 3. RoHS Compliant in accordance with EU Directive(2011/65/EU) 4. REACH Compliant in accordance with Regulation (EC) No 1907/2006 5. Dimension tolerance: ±0.20mm

Yantel Corporation

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For detailed performance specs & shopping online see Yantel web site : www.yantel-corp.com

Mechanical Outline



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Hybrid Coupler Pin Configuration

The HC0350W03 has an orientation marker to denote Pin 1. Once port one has been identified the other ports are known automatically. Please see the chart below for clarification:

Pin 2 Pin 3				
Configuration	Pin Pin 1	Pin 2	Pin 4 Pin 3	Pin 4
Splitter	Input	-3dB $\angle heta$	-3dB $\angle \theta - 90$	Isolated
Splitter	-3dB $\angle \theta$	Input	Isolated	-3dB $\angle \theta - 90$
Splitter	-3dB $\angle \theta - 90$	Isolated	Input	-3dB $\angle heta$
Splitter	Isolated	-3dB $\angle \theta - 90$	-3dB $\angle heta$	Input
*Combiner	$A \angle \theta - 90$	Output	Isolated	$A \angle heta$
*Combiner	Output	$A \angle \theta - 90$	$A \angle heta$	Isolated
*Combiner	Isolated	$A \angle heta$	$A \angle \theta - 90$	Output
*Combiner	$Aar{ extsf{}} heta$	Isolated	Output	$A \angle \theta - 90$

*Note: "A" is the amplitude of the applied signals. When two quadrature signals with equal amplitudes are applied to the coupler as described in the table, they will combine at the output port. If the amplitudes are not equal, some of the applied energy will be directed to the isolated port.

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Definition of Measured Specifications

Parameter	Definition	Mathematical Representation
VSWR (Voltage Standing Wave Ratio)	The impedance match of the coupler to a 50Ω system. A VSWR of 1:1 is optimal.	$\label{eq:VSWR} = \frac{V_{max}}{V_{min}}$ Vmax = voltage maxima of a standing wave Vmin = voltage minima of a standing wave
Return Loss	The impedance match of the coupler to a 50Ω system. Return Loss is an alternate means to express VSWR.	Return Loss (dB)= 20log $\frac{VSWR + 1}{VSWR - 1}$
Insertion Loss	The input power divided by the sum of the power at the two output ports.	Insertion Loss(dB)= 10log $\frac{P_{in}}{P_{cpl} + P_{transmission}}$
Isolation	The input power divided by the power at the isolated port.	Isolation(dB)= 10log $\frac{P_{in}}{P_{iso}}$
Phase Balance	The difference in phase angle between the two output ports.	Phase at coupled port – Phase at transmisson port
Amplitude Balance	The power at each output divided by the average power of the two outputs.	$10\log \frac{P_{cpl}}{\left(\frac{P_{cpl} + P_{transmission}}{2}\right)} \text{ or } 10\log \frac{P_{transmission}}{\left(\frac{P_{cpl} + P_{transmission}}{2}\right)}$

Test Method

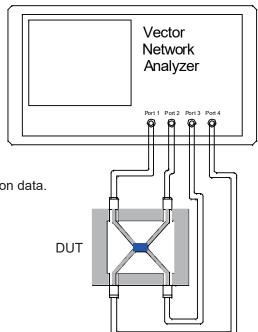
- 1. Calibrating your vector network analyzer.
- 2. Connect the VNA 4 Port to DUT respectively.
- 3. Measure the data of coupling through port 1 to port 4(S41).
- 4. Measure the data of transmission through port 1 to port 3(S31).
- 5. Measure the data of isolation through port 1 to port 2(S21).
- 6. Measure the data of phase port 4 & port 3(port 1 feeding).
- 7. Measure the data of return loss port 1, port 2, port 3 & port 4.
- 8. According to the above data to calculate insertion loss, amplitude balance & phase.

Note:

1. When calculating insertion loss at room temperature,

demo board loss should be removed from both coupling & transmission data. Please refer to the below table for demo board loss :

Frequency Range(MHz)	Demo Board Loss (dB) @25℃
470-860	0.07
800-1000	0.10
1200-1700	0.15
1700-2000	0.15
2000-2300	0.20
2300-2700	0.25



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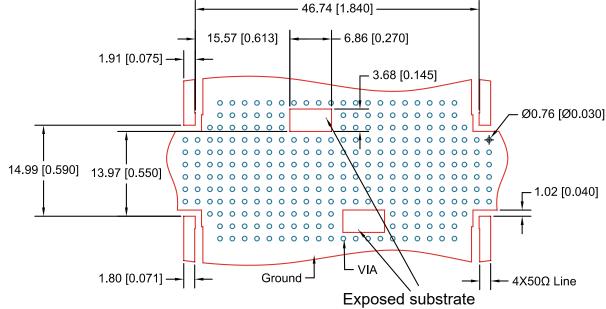
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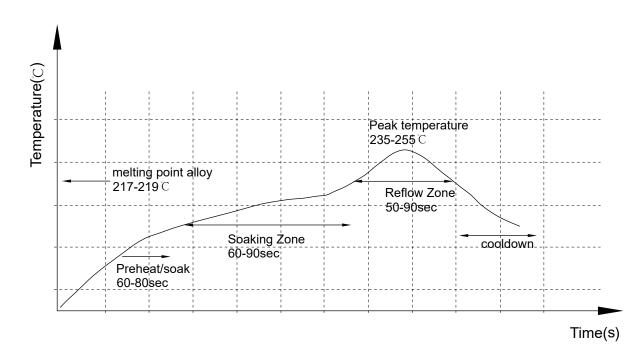
Recommended PCB Layout



NOTE:

- 1. 50 Ω line width is shown above designing from RO4003 dieletric thickness 0.81mm; copper 1 OZ
- 2. Bottom side of the PCB is continuous ground plane.
- 3. All dimensions shown in mm [inch].

Reflow Profile



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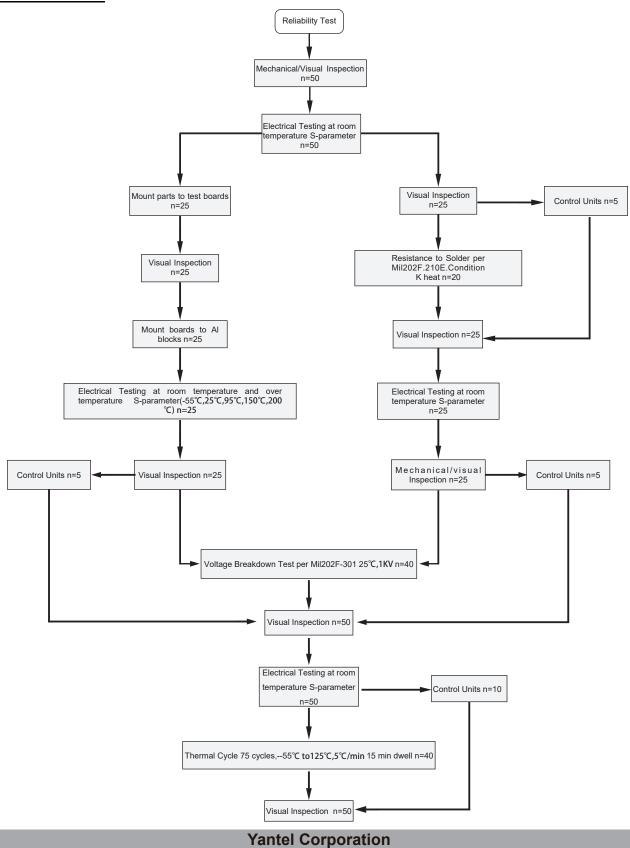
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Reliability Test Flow



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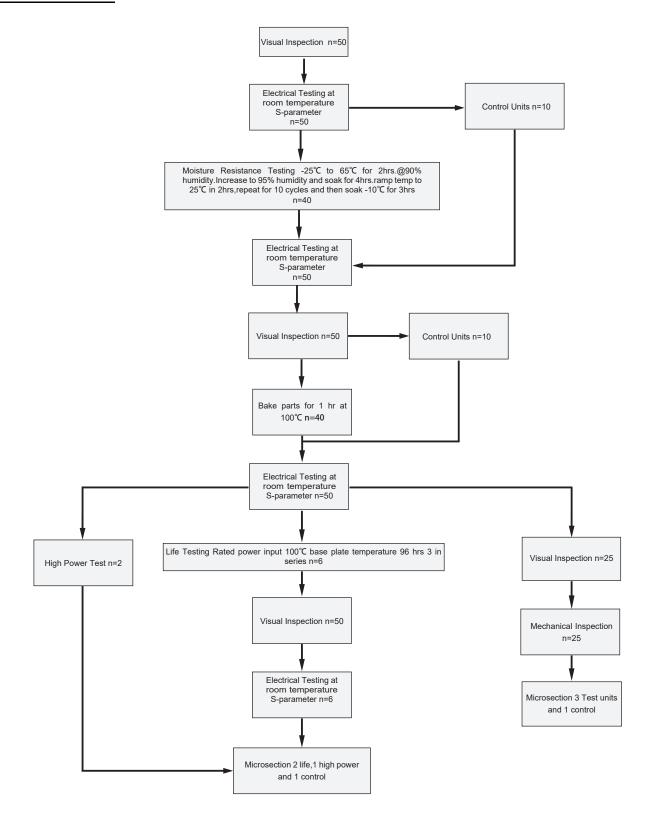
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Reliability Test Flow



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