

Features

- low insertion loss, 0.7 dB typ.
- excellent insertion loss flatness, 0.4 dB peak to peak
- excellent amplitude unbalance, 0.1 dB typ.
- good phase unbalance, 1.1 deg. typ.
- aqueous washable

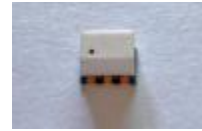
Applications

- instrumentation
- PCS/cellular
- GPS

electrical schematic



YT-ADP-2-20+



2 Way-0° 50Ω 20 to 2000 MHz

Maximum Ratings

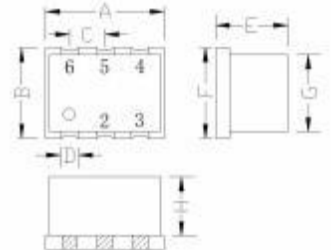
Operating Temperature	-40°C to 85°C
Storage Temperature	-55°C to 100°C
Power Input (as a splitter)	1W max.
Internal Dissipation	0.125W max.

Permanent damage may occur if any of these limits are exceeded.

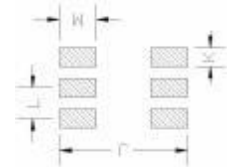
Pin Connections

SUM PORT	1
PORT 1	3
PORT 2	4
GROUND	6
NOT USED	2,5

Outline Drawing



PCB Land Pattern



Suggested Layout,
Tolerance to be within ±0.2

Outline Dimensions: Unit (mm)

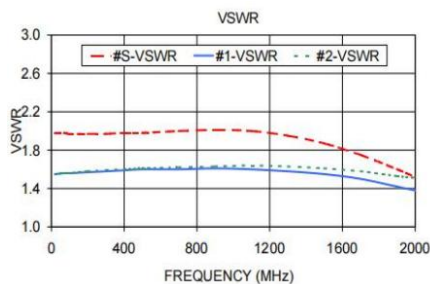
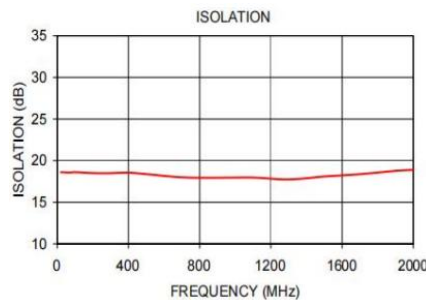
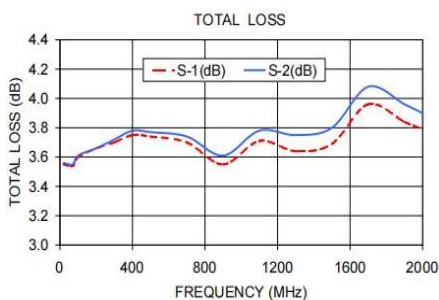
A	8.70	J	8.00
B	6.50	K	1.50
C	2.54	G	5.50
D	1.30	H	4.30
E	5.40	L	2.54
F	6.50	M	2.00
WT	0.5g		

Electrical Specifications

FREQ. RANGE (MHz)	ISOLATION (dB)		INSERTION LOSS ABOVE 3.0 dB		PHASE UNBALANCE (Degrees)		AMPLITUDE UNBALANCE (dB)	
	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.
20-2000	18	15	0.5	1.5	2.0	5.0	0.2	0.7

Typical Performance Data (TEST CONDITIONS: INPUT POWER = 0dBm @ Temperature = +25°C)

Freq. (MHz)	Total Loss (dB)		Amp. Unbal. (dB)	Isolation (dB)	Phase Unbal. (deg.)	VSWRS		
	S-1	S-2				S	1	2
10	3.53	3.52	0.01	18.61	0.01	2.00	1.57	1.57
50	3.55	3.55	0.01	19.19	0.04	1.97	1.59	1.59
100	3.56	3.56	0.00	19.33	0.06	1.97	1.59	1.59
400	3.66	3.64	0.02	19.78	0.09	1.94	1.62	1.62
600	3.73	3.70	0.03	20.10	0.14	1.91	1.63	1.62
800	3.79	3.72	0.07	20.65	0.09	1.88	1.64	1.63
1000	3.85	3.74	0.11	21.64	0.02	1.87	1.66	1.63
1400	3.96	3.69	0.27	24.95	0.59	1.77	1.71	1.65
1600	4.00	3.66	0.34	26.31	0.88	1.74	1.72	1.64
1800	4.05	3.62	0.43	25.64	1.27	1.66	1.75	1.64
2000	4.18	3.65	0.52	23.38	1.59	1.61	1.74	1.63



Suggested PCB Layout

